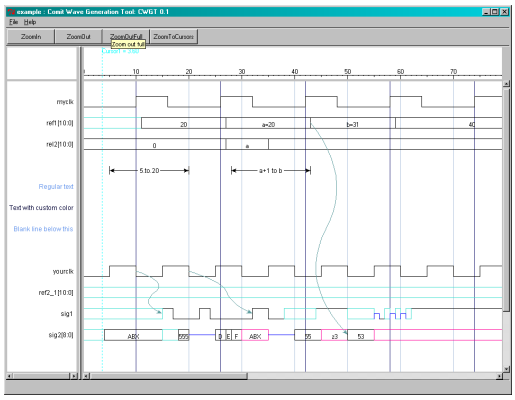
Fiesta® CWGT from Comit
Waveform & Constraints Generation Tool

Fiesta® CWGT saves time by quickly generating signal behavioral depictions with highlighted dependencies and timings from a set of just six commands. The resulting waveforms can be used in design reviews, in creating easy to understand final documentation faster and with more embedded information than capturing screen-shots from conventional waveform generation tools, and are particularly useful in situations such as design document creation where waveform cause-effect relationships need to be understood by a group well before the final code can be ready. Fiesta® CWGT outputs can be directly used to update timing constraints in Comit Fiesta® CSGT synthesis script generator. Fiesta® CWGT speeds up design documentation by producing professional quality waveforms for easy import to text editors.

Benefits

- Saves time by automatically generating and regenerating waveforms through signal behavioral descriptors specified via ASCII file
- Behavioral descriptors specified through a set of just six commands, resulting in little or no learning curve
- Events and trigger specification allows automatic, accurate representation of cause and effect
- Automatic annotation capability allows easy embedding of measures such as setup and hold times and arrows to highlight cause and effect relationships
- Automatic update to Comit Fiesta® CSGT Synthesis Script generator



Key Features

- Draws signals in a variety of formats
- Automatically generates customizable arrows highlighting cause and effect
- Supports infinite number of multi-colored grids based on number of units / clock edges
- Supports in, out and full zoom between cursors
- Prints part or whole waveform to Postscript file
- Copies part or whole waveform to Windows clipboard

Specifications

Inputs

- ASCII text file

Outputs

- Waveform diagrams .PS, .WMF

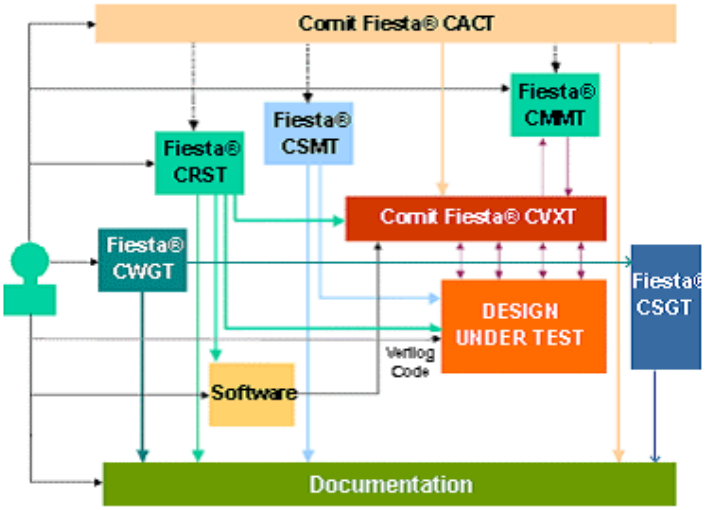
Platforms

OS	Version
Solaris (Sparc)	2.7
Linux	Redhat 7.1
Windows NT	4.0
Windows 95/98/ME	All
Windows 2000/XP	All

Comit Fiesta® CWGT is part of the Comit Fiesta® Process Standardization & Acceleration Toolkit. Individual tools are designed to work in standalone mode or in cascade, where the output of one tool can be used by another.

Fiesta® Process Standardization & Acceleration Toolkit is an integrated set of tools with a vision to painlessly transform specification to product, by producing as much of code and documentation automatically as possible, and simultaneously setting up a compatible verification environment from the start. Designers, therefore, are free to focus on designing state machines and creating tests. Coexists with industry standard EDA tools for simulation, synthesis and layout.

The toolkit consists of the following additional tools:



Fiesta® CSMT Finite State Machine Editor
Generates synthesizable Verilog code, and diagrams for documentation from state machines.

Fiesta® CRST Register Specification Tool
Accepts register bank definitions for a chip. Generates and regenerates documentation, software interface definitions, hardware implementations and verification definitions, preserving consistency, and avoiding errors

Fiesta® CVXT Open Verification Environment
Provides the ability to build parallel, automated, synchronized, self-checking verification testbenches for complex ASIC, SoC and programmable SoC designs. The environment bolts on to industry standard Verilog simulators and supports both real-world system testing and rigorous hardware module level and interface tests

Fiesta® CMMT Simulation Memory Modeler
Generates dynamically configurable simulation time memory models that can be used in advanced system-level verification

Fiesta® CSGT Synthesis Script Generator
Accepts constrains and generates script to automate synthesis flow for popular synthesis tools

Fiesta® CAVT AHDL to VHDL Conversion tool
Converts Altera's proprietary HDL - AHDL to portable VHDL files to target any technology

Fiesta® CACT Architectural Code Generation Tool
Accepts block level architectural input including third party IP and generates implementation roadmap by defining placeholders for all modules and interfaces.

Fiesta® Process Standardization and Acceleration Tool Kit is an industrial strength suite of tools designed, developed, tested and used by engineers of Comit's Contract Engineering Center. Their experience in developing processes and methodology that yield predictable and accurate results forms the foundation of the toolkit. Use it with confidence.

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